

Micropower Regulator and Comparator

FEATURES

- Input Voltage Range: 4.5V to 36V
- 40μA Supply Current
- 125mA Output Current
- 2.5V Reference Voltage
- Reference Output Sources 1mA and Sinks 0.5mA
- Dual Output Comparator
- Comparator Sinks 10mA
- Dropout Detector
- 0.2V Dropout Voltage
- Thermal Limiting
- Available in SO Package

APPLICATIONS

- Battery Systems
- Battery Backup Systems
- Portable Terminals
- Portable Instruments

DESCRIPTION

The LT[®]1020 is a combination micropower positive regulator and free collector comparator on a single monolithic chip. With only 40μA supply current, the LT1020 can supply over 125mA of output current. Input voltage range is from 4.5V to 36V and dropout voltage is 0.6V at 125mA. Dropout voltage decreases with lower load currents. Also included on the chip is a class B output 2.5V reference that can either source or sink current. A dropout detector provides an output current to indicate when the regulator is about to drop out of regulation.

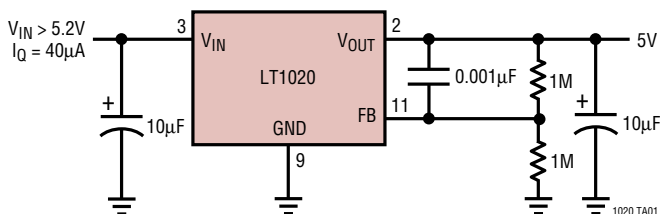
The dual output comparator can be used as a comparator for system or battery monitoring. For example, the comparator can be used to warn of low system voltage while the dropout detector shuts down the system to prevent abnormal operation. Frequency compensation of the comparator for amplifier applications can be obtained by adding external output capacitance. Dual output or positive and negative regulators can also be made.

The 2.5V reference will source or sink current. This allows it to be used as a supply splitter or auxiliary output.

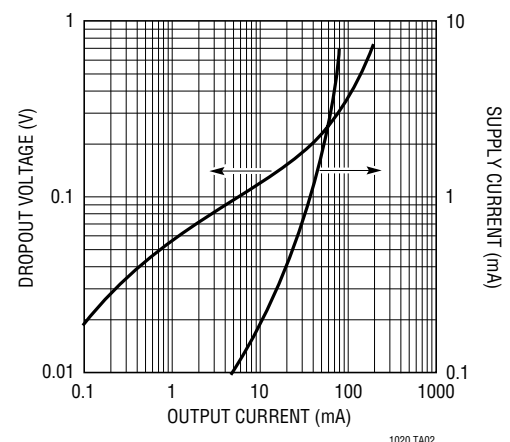
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TYPICAL APPLICATION

5V Regulator



Dropout Voltage and Supply Current



ABSOLUTE MAXIMUM RATINGS (Note 1)

Input Voltage 36V
 NPN Collector Voltage 36V
 PNP Collector Voltage Supply – 36V
 Output Short Circuit Duration Indefinite
 Power Dissipation Internally Limited
 Storage Temperature Range –65°C to 150°C

Operating Temperature Range

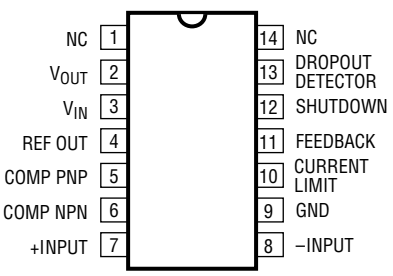
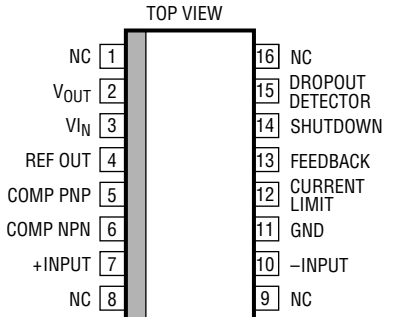
LT1020C 0°C to 100°C

LT1020I –40°C to 100 °C

LT1020M (**OBSOLETE**) –55°C to 125°C

Lead Temperature (Soldering, 10 sec) 300°C

PACKAGE/ORDER INFORMATION

TOP VIEW  N PACKAGE 14-LEAD PDIP $T_{JMAX} = 110^{\circ}\text{C}, \theta_{JA} = 130^{\circ}\text{C/W}$	ORDER PART NUMBER LT1020CN LT1020IN	TOP VIEW  SW PACKAGE 16-LEAD PLASTIC (WIDE) SO $T_{JMAX} = 110^{\circ}\text{C}, \theta_{JA} = 150^{\circ}\text{C/W}$	ORDER PART NUMBER LT1020CSW LT1020ISW
J PACKAGE 14-LEAD CERDIP $T_{JMAX} = 150^{\circ}\text{C}, \theta_{JA} = 80^{\circ}\text{C/W}$ OBSOLETE PACKAGE Consider the N14 Package for Alternate Source	LT1020CJ LT1020IJ LT1020MJ		

Consult LTC Marketing for parts specified with wider operating temperature ranges.

ELECTRICAL CHARACTERISTICS $T_J = 25^{\circ}\text{C}$

PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
Reference					
Reference Voltage	$4.5\text{V} \leq V_{IN} \leq 36\text{V}$	2.46	2.50	2.54	V
Line Regulation	$4.5\text{V} \leq V_{IN} \leq 36\text{V}$		0.01	0.015	%/V
Load Regulation	$-0.5\text{mA} \leq I_{REF} \leq 1\text{mA}, V_{IN} = 12\text{V}$		0.2	0.3	%
Output Source Current	$V_{IN} = 5\text{V}$	1	4		mA
Output Sink Current	$V_{IN} = 5\text{V}$	0.5	2		mA
Temperature Stability			1		%
Regulator					
Supply Current	$V_{IN} = 6\text{V}, I_{OUT} \leq 100\mu\text{A}$		45	80	μA
	$V_{IN} = 36\text{V}, I_{OUT} \leq 100\mu\text{A}$		75	120	μA
	$V_{IN} = 12\text{V}, I_{OUT} = 125\text{mA}$		11	20	mA
Output Current	$(V_{IN} - V_{OUT}) \geq 1\text{V}, V_{IN} \geq 6\text{V}$	125			mA
Load Regulation	$(V_{IN} - V_{OUT}) \geq 1\text{V}, V_{IN} \geq 6\text{V}$		0.2	0.5	%
Line Regulation	$6\text{V} \leq V_{IN} \leq 36\text{V}$		0.01	0.015	%/V
Dropout Voltage	$I_{OUT} = 100\mu\text{A}$		0.02	0.05	V
	$I_{OUT} = 125\text{mA}$		0.4	0.65	V

sn1020 1020fcs

ELECTRICAL CHARACTERISTICS $T_J = 25^\circ\text{C}$

PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
Regulator					
Feedback Sense Voltage	$V_{IN} = 12\text{V}$	2.44	2.5	2.56	V
Dropout Detector Current	$\Delta V_{OUT} = -0.05\text{V}$, $I_{OUT} = 500\mu\text{A}$	3	20		μA
Feedback Bias Current			15	40	nA
Minimum Load Current	$V_{IN} = 36\text{V}$		1	5	μA
Short-Circuit Current	$V_{IN} = 36\text{V}$ Pins 9 and 10 Shorted, $V_{IN} = 4.5\text{V}$	3	300 30	400	mA mA
Comparator					
Offset Voltage	$0\text{V} \leq V_{CM} \leq 35\text{V}$, $V_{IN} = 36\text{V}$		3	7	mV
Bias Current	$0\text{V} \leq V_{CM} \leq 35\text{V}$, $V_{IN} = 36\text{V}$		15	40	nA
Offset Current	$0\text{V} \leq V_{CM} \leq 35\text{V}$, $V_{IN} = 36\text{V}$		4	15	nA
Gain-NPN Pull-Down	$\Delta V_{OUT} = 29\text{V}$, $R_L = 20\text{k}$	2000	10000		V/V
Common Mode Rejection	$0\text{V} \leq V_{CM} \leq 35\text{V}$, $V_{IN} = 36\text{V}$	80	94		dB
Power Supply Rejection	$4.5\text{V} \leq V_S \leq 36\text{V}$	80	96		dB
Output Sink Current	$V_{IN} = 4.5\text{V}$	10	18		mA
NPN Saturation Voltage	$I_{OUT} = 1\text{mA}$		0.4	0.6	V
Output Source Current		60	200		μA
Input Voltage Range		0		$V_{IN} - 1$	V
Response Time			5		μs
Leakage Current (NPN)				2	μA

The ● denotes the specifications which apply over the full operating temperature range, otherwise specifications are at $T_J = 25^\circ\text{C}$.

PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
Reference					
Reference Voltage	$4.5\text{V} \leq V_{IN} \leq 36\text{V}$	● 2.40	2.50	2.55	V
Line Regulation	$4.5\text{V} \leq V_{IN} \leq 36\text{V}$	●	0.01	0.02	%/V
Load Regulation	$-0.5\text{mA} \leq I_{REF} \leq 1\text{mA}$, $V_{IN} = 12\text{V}$	●	0.3	0.4	%
Output Source Current	$V_{IN} = 5\text{V}$	● 1			mA
Output Sink Current	$V_{IN} = 5\text{V}$	● 0.5			mA
Regulator					
Supply Current	$V_{IN} = 6\text{V}$, $I_{OUT} \leq 100\mu\text{A}$	●	65	95	μA
	$V_{IN} = 36\text{V}$, $I_{OUT} \leq 100\mu\text{A}$	●	85	120	μA
	$V_{IN} = 12\text{V}$, $I_{OUT} = 125\text{mA}$	●	11	20	mA
Output Current	$(V_{IN} - V_{OUT}) \geq 1\text{V}$, $V_{IN} \geq 6\text{V}$	● 125			mA
Load Regulation	$(V_{IN} - V_{OUT}) \geq 1\text{V}$, $V_{IN} \geq 6\text{V}$	●		1	%
Line Regulation	$6\text{V} \leq V_{IN} \leq 36\text{V}$	●		0.02	%/V
Dropout Voltage	$I_{OUT} = 100\mu\text{A}$	●		0.06	V
	$I_{OUT} = 125\text{mA}$	●		0.85	V
Feedback Sense Voltage	$V_{IN} = 12\text{V}$	● 2.38	2.5	2.57	V
Dropout Detector Current	$\Delta V_{OUT} = -0.05\text{V}$, $I_{OUT} = 500\mu\text{A}$	● 3			μA
Feedback Bias Current		●		50	nA
Minimum Load Current	$V_{IN} = 36\text{V}$	●		50	μA
Short-Circuit Current	$V_{IN} = 36\text{V}$	●	300	400	mA
	Pins 9 and 10 Shorted, $V_{IN} = 4.5\text{V}$	● 2.5	30		mA
Comparator					
Offset Voltage	$0\text{V} \leq V_{CM} \leq 35\text{V}$, $V_{IN} = 36\text{V}$	●		10	mV
Bias Current	$0\text{V} \leq V_{CM} \leq 35\text{V}$, $V_{IN} = 36\text{V}$ (Note 2)	●	15	60	nA

ELECTRICAL CHARACTERISTICS

The ● denotes the specifications which apply over the full operating temperature range, otherwise specifications are at $T_J = 25^\circ\text{C}$.

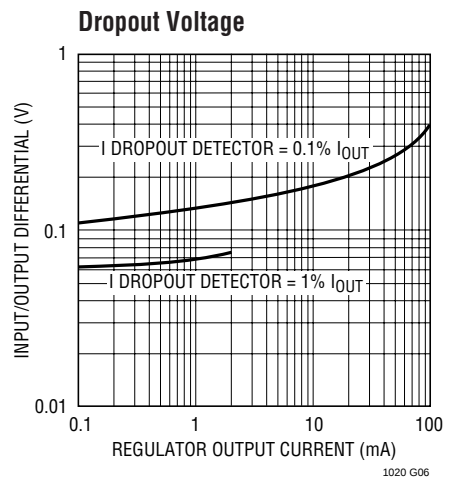
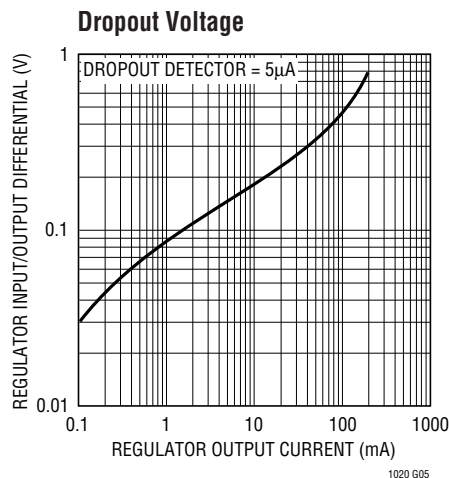
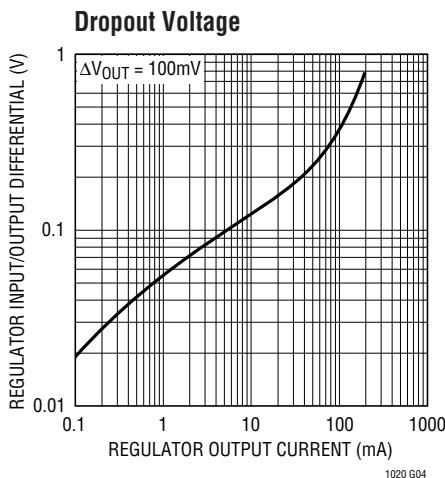
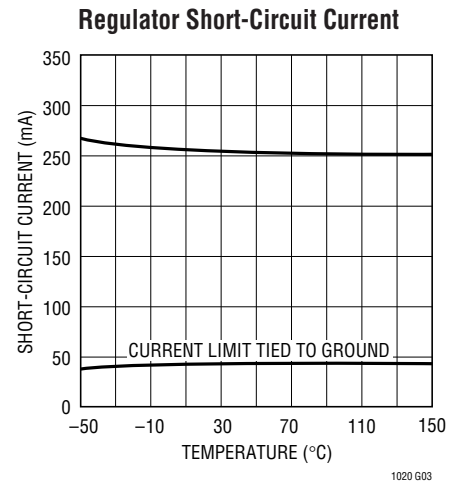
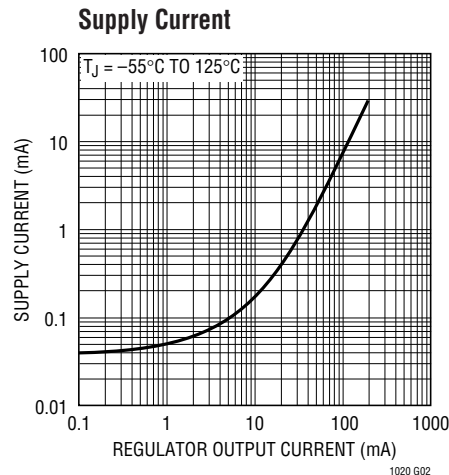
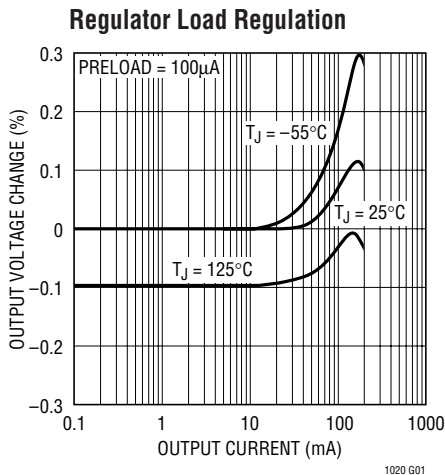
PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
Comparator					
Offset Current	$0\text{V} \leq V_{CM} \leq 35\text{V}$, $V_{IN} = 36\text{V}$	●		20	nA
Gain-NPN Pull-Down	$\Delta V_{OUT} = 29\text{V}$, $R_L = 20\text{k}$	●	1000		V/V
Common Mode Rejection	$0\text{V} \leq V_{CM} \leq 35\text{V}$, $V_{IN} = 36\text{V}$	●	80		dB
Power Supply Rejection	$4.5\text{V} \leq V_{IN} \leq 36\text{V}$	●	80		dB
Output Sink Current	$V_{IN} = 4.5\text{V}$ (Note 3)	●	5	10	mA
Output Source Current		●	40	120	μA
Input Voltage Range		●	0	$V_{IN} - 1$	V
Leakage Current (NPN)	$V_{IN} = 36\text{V}$	●		8	μA

Note 1: Absolute Maximum Ratings are those values beyond which the life of a device may be impaired.

Note 2: For $0\text{V} \leq V_{CM} \leq 0.1\text{V}$ and $T > 85^\circ\text{C}$ $I_{BIAS(MAX)}$ is 100nA.

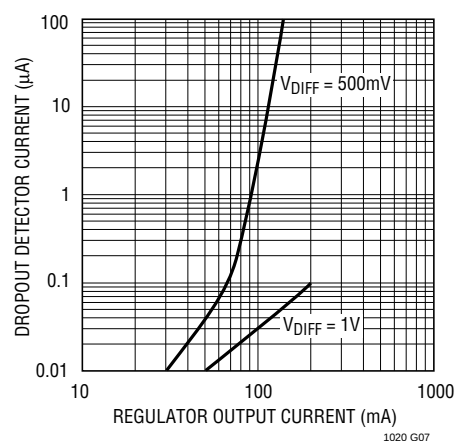
Note 3: For $T_A \leq -40^\circ\text{C}$ output $I_{SINK(MIN)}$ is 2.5mA.

TYPICAL PERFORMANCE CHARACTERISTICS

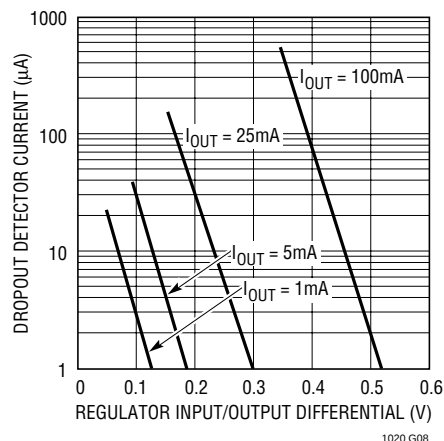


TYPICAL PERFORMANCE CHARACTERISTICS

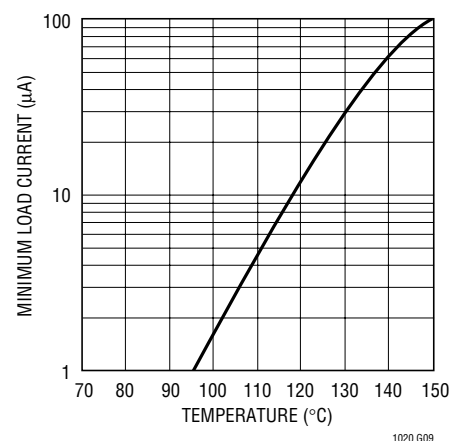
Dropout Detector Current



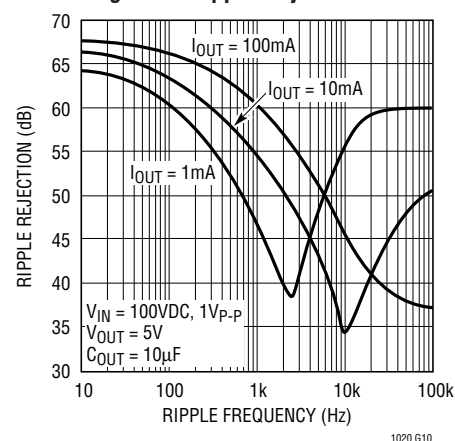
Dropout Detector Current



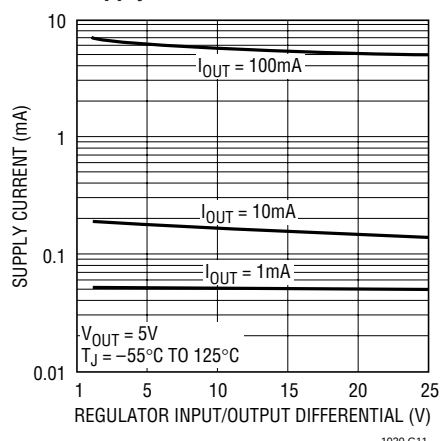
Regulator Minimum Load Current



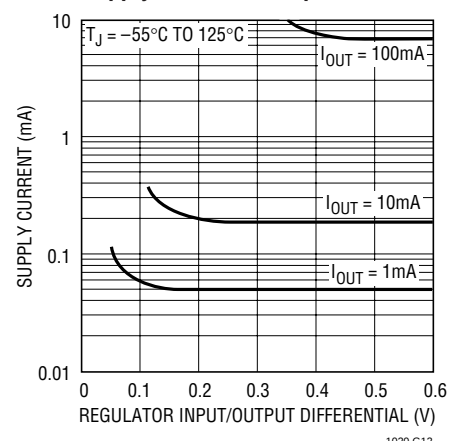
Regulator Ripple Rejection



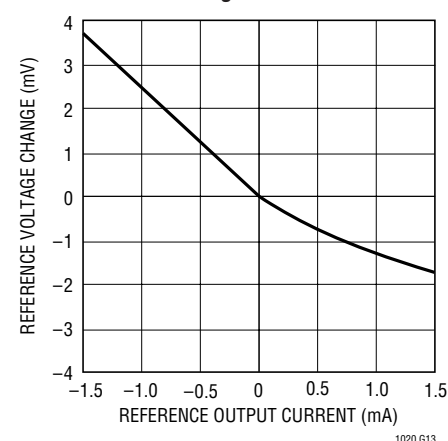
Supply Current



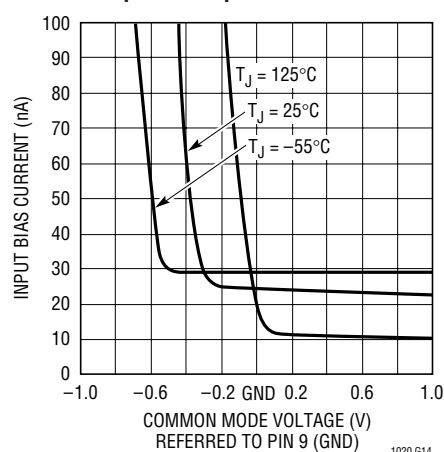
Supply Current at Dropout



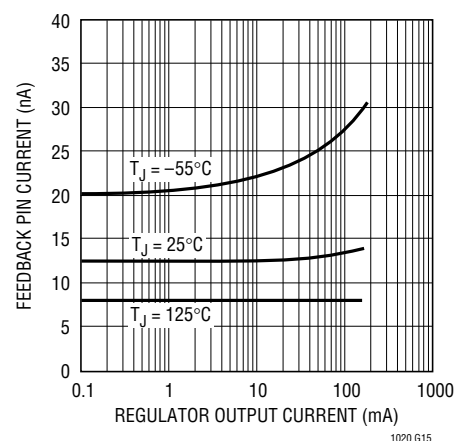
Reference Regulation



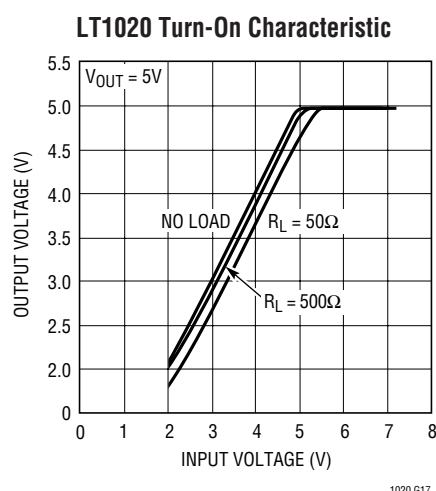
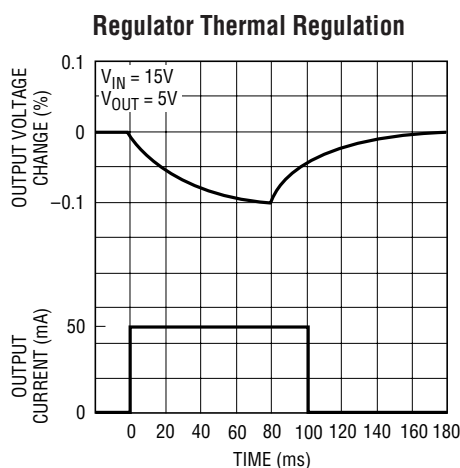
Comparator Input Bias Current



Feedback Pin Current



TYPICAL PERFORMANCE CHARACTERISTICS



PIN FUNCTIONS

Pins 1, 14: No Internal Connection.

Pin 2: Regulator Output. Main output, requires 10 μ F output capacitor. Can be shorted to V_{IN} or ground without damaging the device.

Pin 3: Input Supply. Bypass with 10 μ F capacitor. Must always be more positive than ground.

Pin 4: Reference. 2.5V can source or sink current. May be shorted to ground or up to 5V. Voltages in excess of 5V can damage the device.

Pin 5: Comparator PNP Output. Open Collector PNP Output. Pull-up current source for the comparator. May be connected to any voltage from V_{IN} to 36V more negative than V_{IN} (operates below ground). Short-circuit protected. For example, if V_{IN} is 6V then pin 5 will operate to -30V.

Pin 6: Comparator NPN Output. Open Collector NPN Output. May be connected to any voltage from ground to 36V more positive than ground (operates above V_{IN}). Short-circuit protected.

Pins 7, 8: Comparator Inputs. Operates from ground to $V_{IN} - 1V$. Comparator inputs will withstand 36V even with V_{IN} of 0V.

Pin 9: Ground.

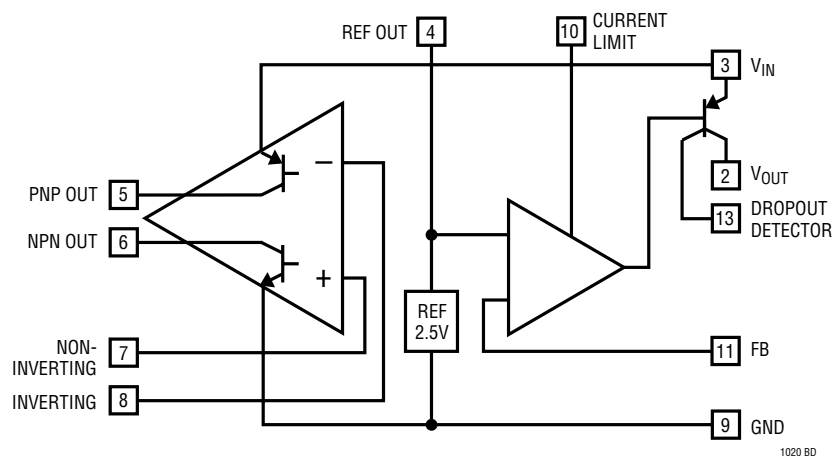
Pin 10: Current Limit. Connecting this pin to ground decreases the regulator current limit to 3mA minimum. Leave open when not used.

Pin 11: Feedback. This is the feedback point of the regulator. When operating, it is nominally at 2.5V. Optimum source resistance is 200k to 500k. The feedback pin should not be driven below ground or more positive than 5V.

Pin 12: Shutdown. Turns output off.

Pin 13: Dropout Detector. This pin acts like a current source from V_{IN} which turns on when the output transistor goes into saturation. The magnitude of the current depends on the magnitude of the output current and the input/output voltage differential. Pin current ranges from 5 μ A to about 300 μ A.

BLOCK DIAGRAM



APPLICATIONS INFORMATION

The LT1020 is especially suited for micropower system applications. For example, the comparator section of the LT1020 may be used as a battery checker to provide an indication of low battery. The dropout detector can shut down the system when the battery voltage becomes too low to regulate. Another type of system application for the LT1020 would be to generate the equivalent of split supplies from a single power input. The regulator section provides regulated output voltage and the reference, which can both source and sink current, is then an artificial system ground providing a split supply for the system.

For many applications the comparator can be frequency compensated to operate as an amplifier. Compensation values for various gains are given in the data sheet. The comparator gain is purposely low to make it easier to frequency compensate as an amplifier. Two outputs are available on the comparator, the NPN output is capable of sinking 10mA and can drive loads connected to voltages in excess of the positive power supply. This is useful for driving switches or linear regulators from a higher input voltage. The PNP output, which is capable of sourcing 100 μ A can drive loads below ground. It can be used to make negative regulators with the addition of an external pass transistor. Both outputs can be tied together to provide an output that swings from rail-to-rail for comparator or amplifier applications. Although it is not specified, the gain for the PNP output is about 500 to 1000.

If the PNP output is being used, to maximize the gain a 1 μ A to 5 μ A load should be placed upon the NPN output collector. This is easily done by connecting a resistor between the NPN collector and the reference output. (Providing this operating current to the NPN side increases the internal emitter base voltages and maximizes the gain of the PNP stage.) Without this loading on the NPN collector, at temperatures in excess of 75°C, the gain of the PNP collector can decrease by a factor of 2 or 3.

Reference

Internal to the LT1020 is a 2.5V trimmed class B output reference. The reference was designed to be able to source or sink current so it could be used in supply splitting applications as well as a general purpose reference for external circuitry. The design of the reference allows it to source typically 4mA or 5mA and sink 2mA. The available source and sink current decreases as temperature increases. It is sometimes desirable to decrease the AC output impedance by placing an output capacitor on them. The reference in the LT1020 becomes unstable with large capacitive loads placed directly on it. When using an output capacitor, about 20 Ω should be used to isolate the capacitor from the reference pin. This 20 Ω resistor can be placed directly in series with the capacitor or alternatively the reference line can have 20 Ω placed in series with it and then a capacitor to ground. This is shown in Figure 1. Other

APPLICATIONS INFORMATION

than placing large capacitive loads on the reference, no other precautions are necessary and the reference is stable with nominal stray capacitances.

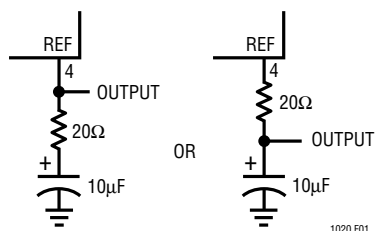


Figure 1. Bypassing Reference

Overload Protection

The main regulator in the LT1020 is current limited at approximately 350mA. The current limit is stable with both input voltage and temperature. A current limit pin, when strapped to ground, decreases the output current. This allows the output current to be set to a lower value than 250mA. The output current available with the current limit pin strapped to ground is not well controlled so if precise current limiting is desired it should be provided externally as is shown in some of the application circuits.

If the device is overloaded for long periods of time, thermal shutdown turns the output off. In thermal shutdown, there may be some oscillations which can disturb external circuitry. A diode connected between the reference and feedback terminal provides hysteresis under thermal shutdown, so that the device turns on and off with about a 5 second period and there are no higher frequency oscillations. This is shown in Figure 2. This diode is recommended for most applications. Thermal shutdown temperature is set at approximately 145°C.

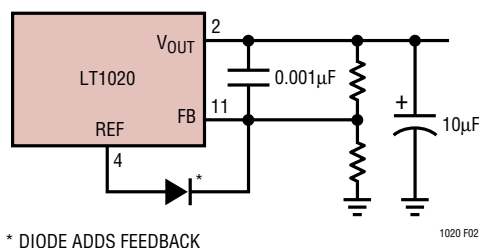


Figure 2. Minimizing Oscillation in Thermal Shutdown

Like most other IC regulators, a minimum load is required on the output of the LT1020 to maintain regulation. For most standard regulators this is normally specified at 5mA. Of course, for a micropower regulator this would be a tremendously large current. The output current must be large enough to absorb all the leakage current of the pass transistor at the maximum operating temperature. It also affects the transient response; low output currents have long recovery times from load transients. At high operating temperatures the minimum load current increases and having too low of a load current may cause the output to go unregulated. Devices are tested for minimum load current at high temperature. The output voltage setting resistors to the feedback terminal can usually be used to provide the minimum load current.

Frequency Compensation

The LT1020 is frequency compensated by a dominant pole on the output. An output capacitor of 10μF is usually large enough to provide good stability. Increasing the output capacitor above 10μF further improves stability. In order to insure stability, a feedback capacitor is needed between the output pin and the feedback pin. This is because stray capacitance can form another pole with the large value of feedback resistors used with the LT1020. Also, a feedback capacitor minimizes noise pickup and improves ripple rejection.

With the large dynamic operating range of the output current, 10000:1, frequency response changes widely. Low AC impedance capacitors are needed to insure stability. While solid tantalum are best, aluminum electrolytics can be used but larger capacitor values may be needed.

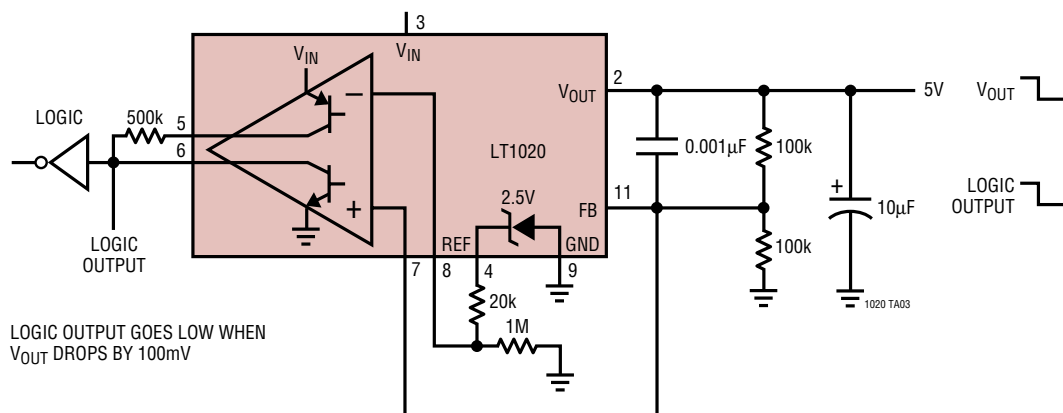
The CURRENT LIMIT pin allows one of the internal nodes to be rolled off with a 0.05μF capacitor to ground. With this capacitor, lower values of regulator output capacitance can be used (down to 1μF) for low (<20mA) output currents. Values of capacitance greater than 0.05μF degrade the transient response, so are not recommended.

If the CURRENT LIMIT pin is connected to GND, the current limit is decreased and only a 1μF output capacitor is needed.

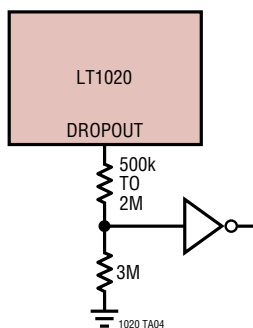
When bypassing the reference, a 20Ω resistor must be connected in series with the capacitor.

TYPICAL APPLICATIONS

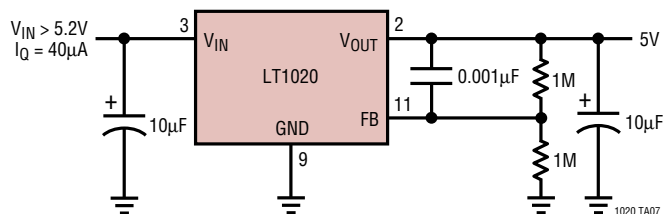
Regulator with Output Voltage Monitor



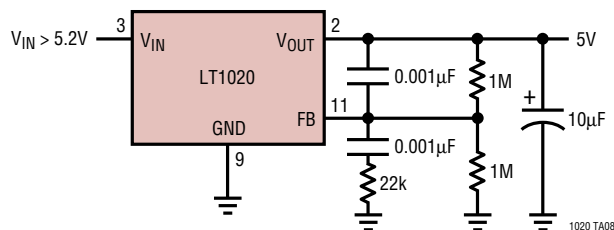
Driving Logic with Dropout Detector



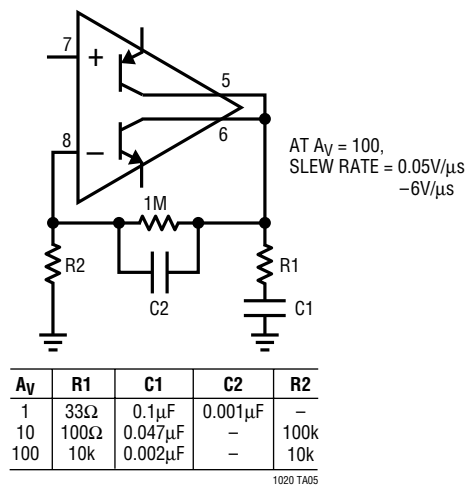
5V Regulator



Regulator with Improved Transient Response

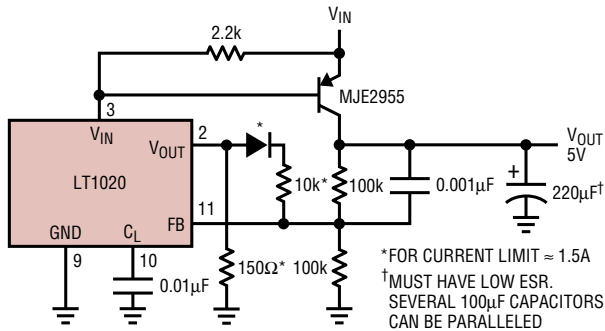


Compensating the Comparator As an Op Amp



TYPICAL APPLICATIONS

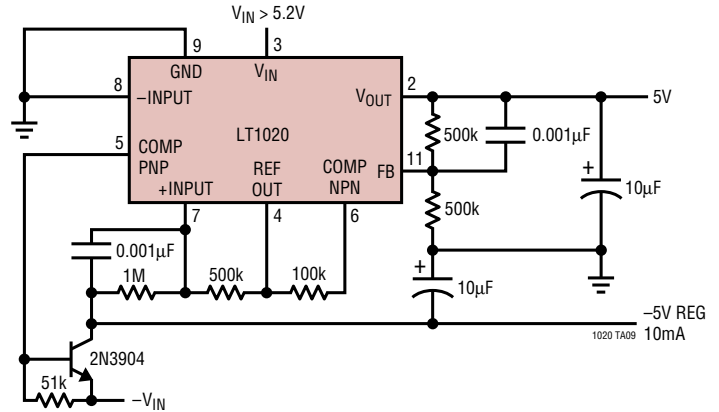
1 Amp Low Dropout Regulator



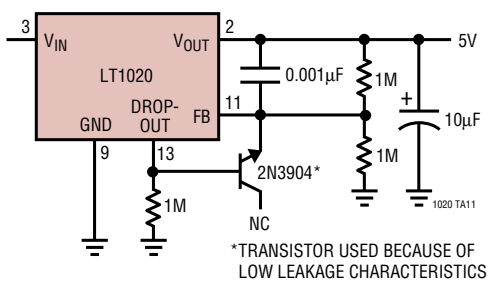
SEE LT1129 DATA SHEET FOR 700mA OUTPUT

1020 TA06

Dual Output Regulator

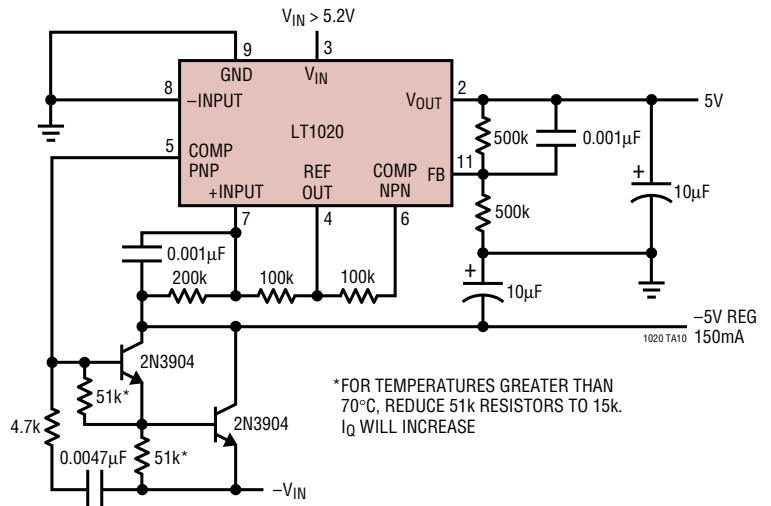


Maintaining Lowest I_Q at Dropout



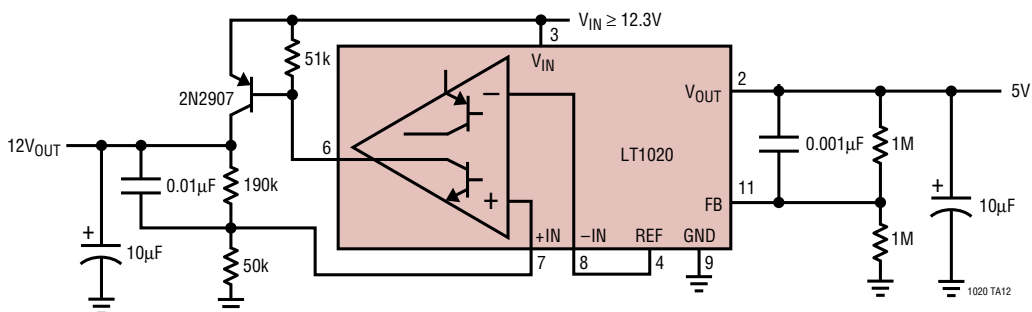
1020 TA11

Dual Output 150mA Regulator



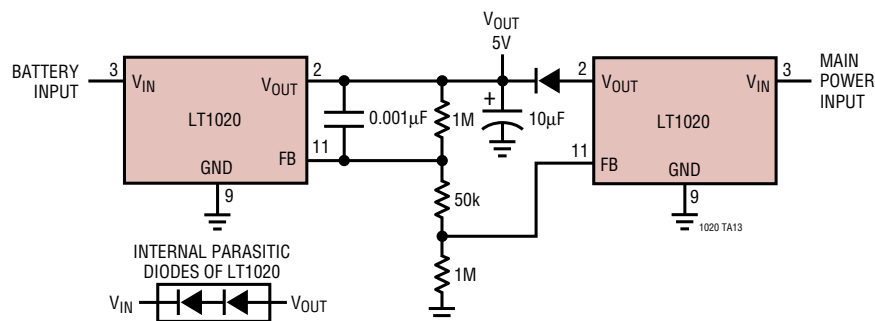
1020 TA10

Dual Output Positive Regulator

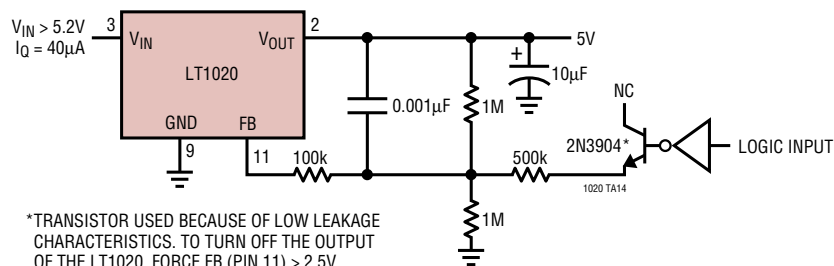


TYPICAL APPLICATIONS

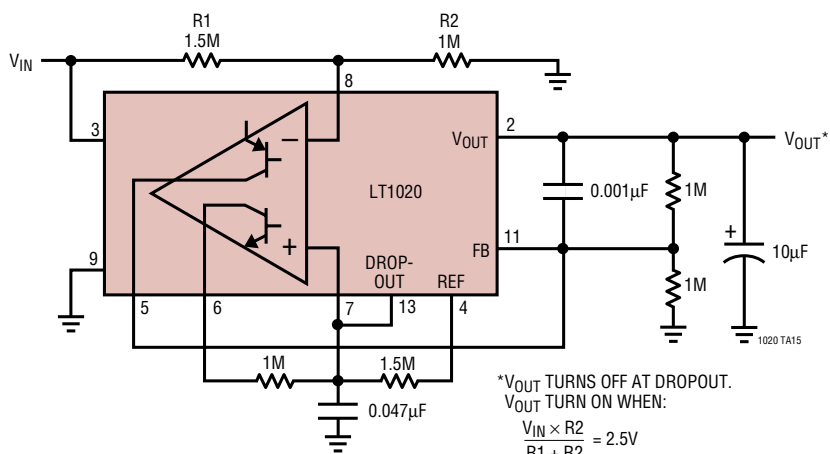
Battery Backup Regulator



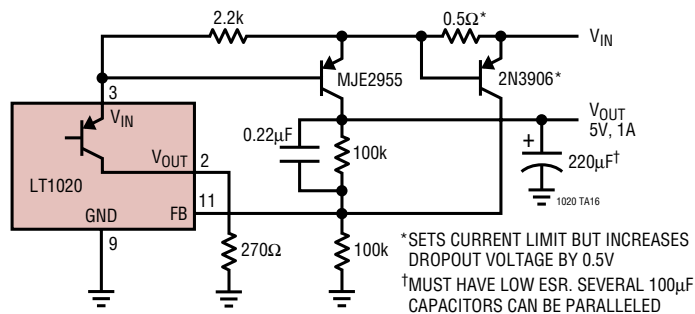
5V Regulator with Shutdown



Turn-Off at Dropout



Current Limited 1 Amp Regulator



The diagram shows a precision current source circuit. An LT1020 operational amplifier is configured with its non-inverting input (+) connected to a 1.2k resistor and its inverting input (-) connected to a 100k resistor and the wiper of a 0.03Ω potentiometer. The potentiometer's other end is connected to the output (V_{OUT}), which is also connected to a load resistor R_L. The op-amp's output (pin 11) drives the base of an NPN MJE2955 transistor. The transistor's emitter is connected to ground, and its collector is connected to a 12V supply (V_{IN}) through a 2.2k resistor. A 100μF capacitor is connected between the collector and ground. A 0.22μF capacitor is connected between the collector and the feedback pin (pin 2). The feedback pin (pin 2) is also connected to a 100k resistor and a 270Ω resistor. The 270Ω resistor is connected to the wiper of the 0.03Ω potentiometer. A 1N4148 diode is connected between the feedback pin (pin 2) and the non-inverting input (+). The op-amp's pins 4 and 5 are connected to ground, and pin 9 is connected to the positive supply rail. The circuit is powered by a 12V supply (V_{IN}) and a 5V supply (V_{OUT}).

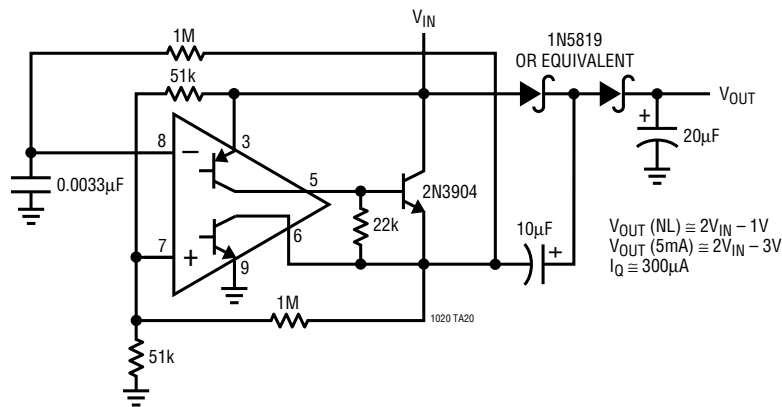
†MUST HAVE LOW ESR. SEVERAL 100μF CAPACITORS CAN BE PARALLELED

[illegible]

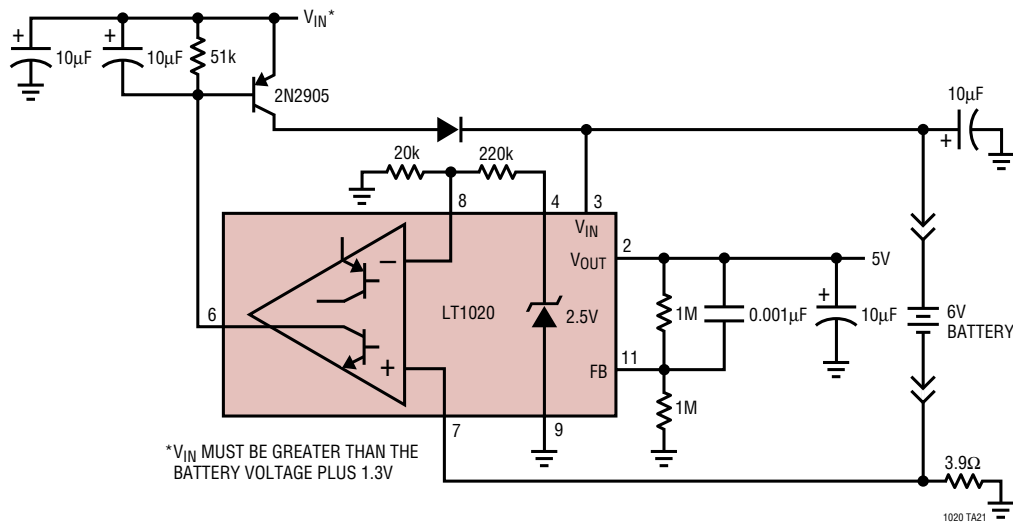
$V_{OUT} (NL) \cong -(V_{IN} - 1V)$
 $V_{OUT} (5mA) \cong -(V_{IN} - 3V)$
 $I_Q \cong 300\mu A$

TYPICAL APPLICATIONS

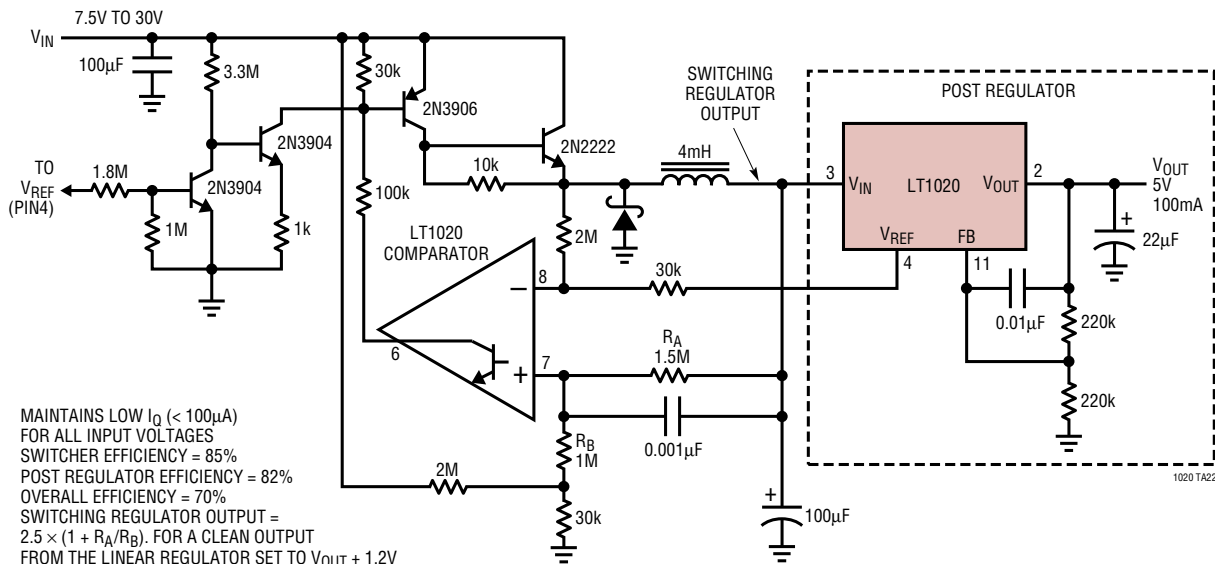
Charge Pump Voltage Doubler



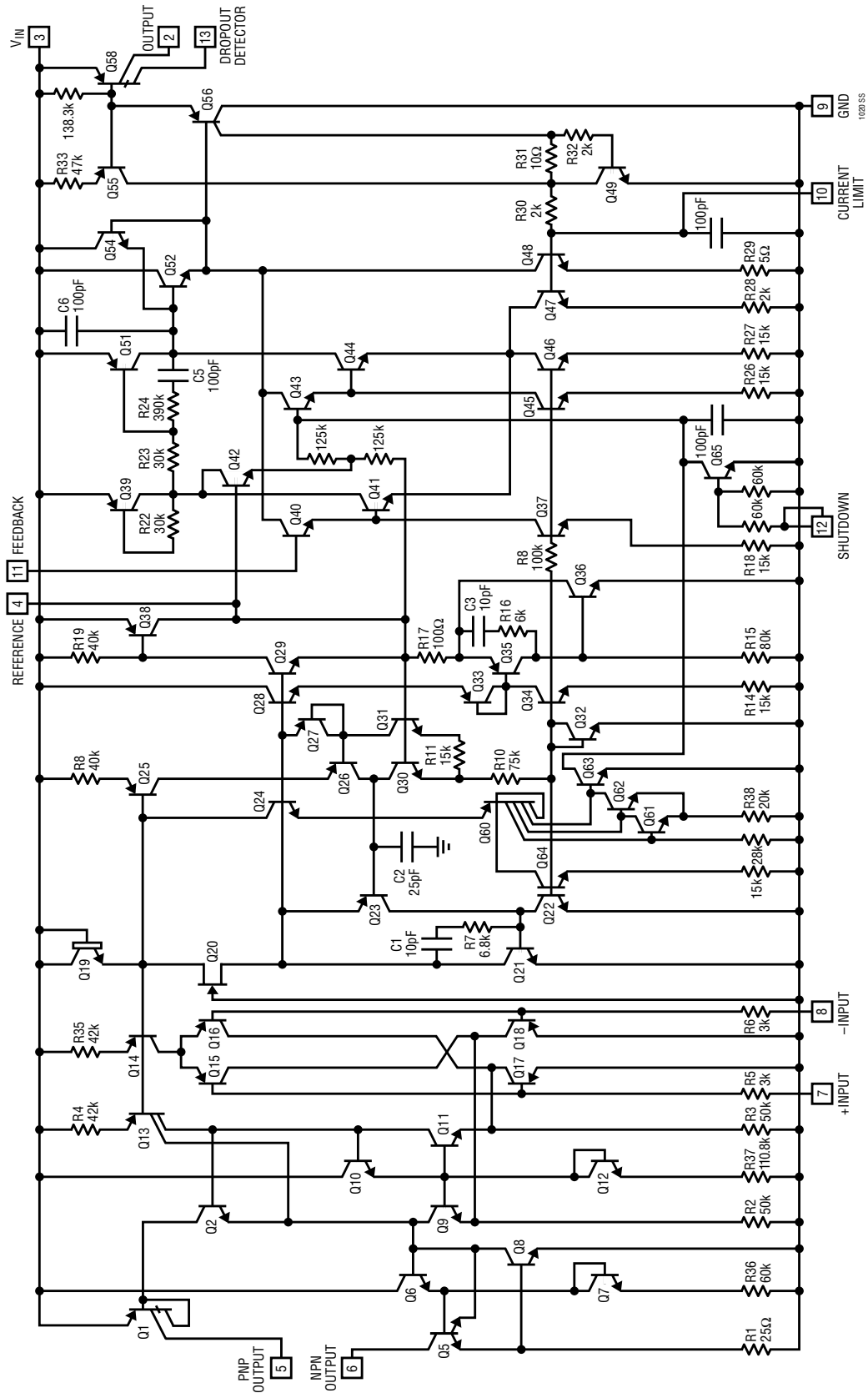
50mA Battery Charger and Regulator



Switching Preregulator for Wide Input Voltage Range

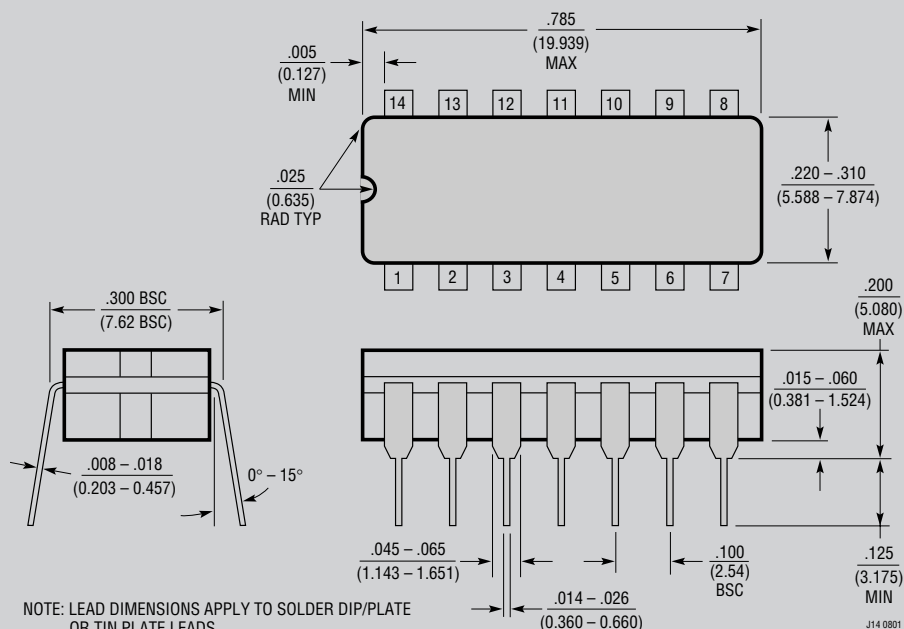


SCHEMATIC DIAGRAM



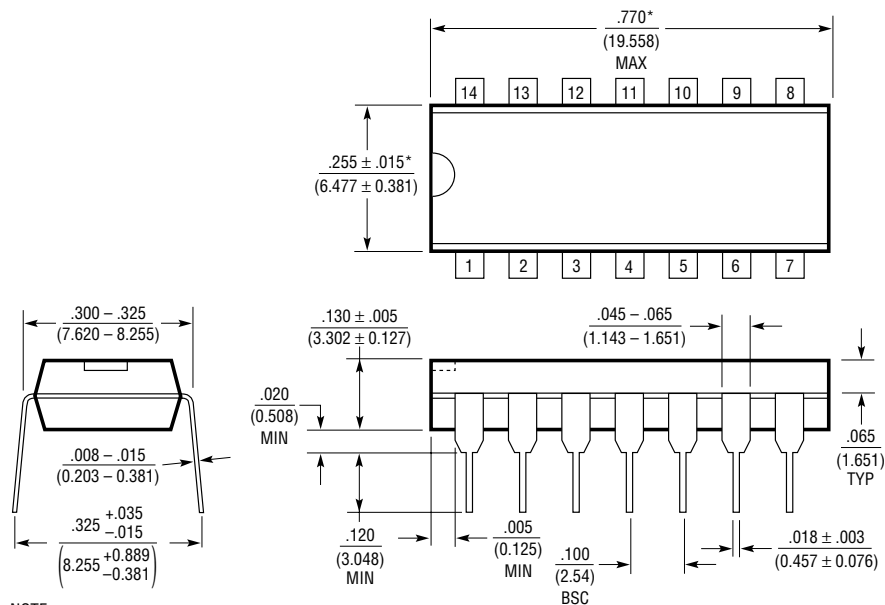
PACKAGE DESCRIPTION

J Package 14-Lead Cerdip (Narrow .300 Inch, Hermetic) (Reference LTC DWG # 05-08-1110)



OBsolete PACKAGE

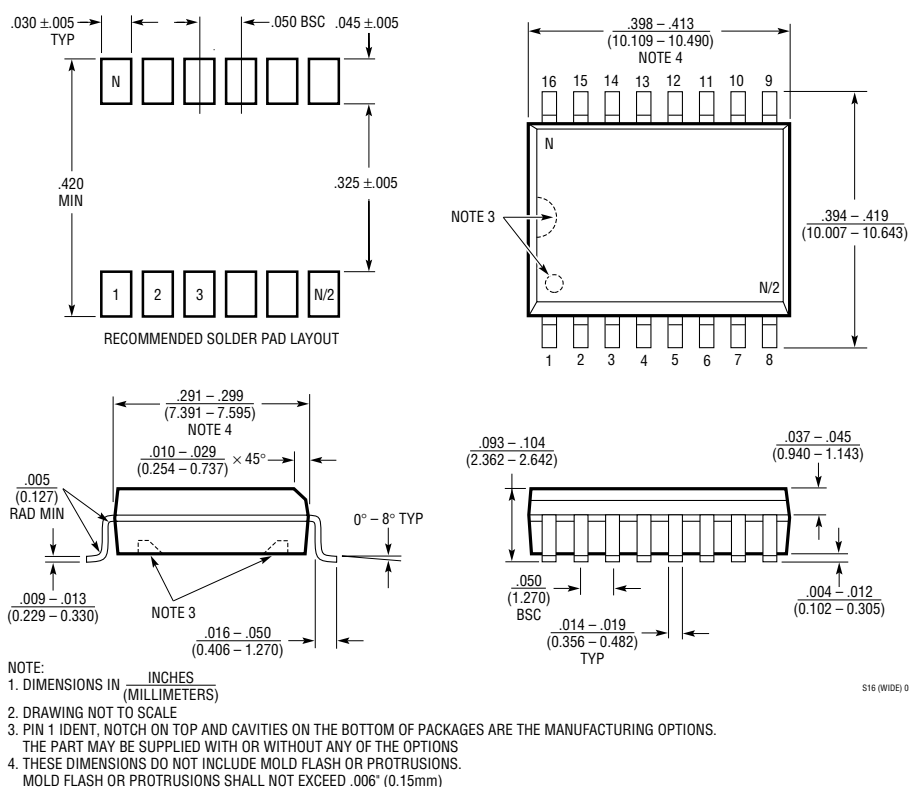
N Package 14-Lead PDIP (Narrow .300 Inch) (Reference LTC DWG # 05-08-1510)



NOTE:
1. DIMENSIONS ARE INCHES / MILLIMETERS
*THESE DIMENSIONS DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS.
MOLD FLASH OR PROTRUSIONS SHALL NOT EXCEED .010 INCH (0.254mm)

PACKAGE DESCRIPTION

SW Package 16-Lead Plastic Small Outline (Wide .300 Inch) (Reference LTC DWG # 05-08-1620)



RELATED PARTS

PART NUMBER	DESCRIPTION	COMMENTS
LT1120/LT1120A	125mA, Micropower Regulator and Comparators	V_{IN} : 4.5V to 36V; $V_{OUT(MIN)}$: 2.50V, Dropout Voltage (V at I_{OUT}): 0.40V; I_Q (Supply): 40 μ A; V_{OUT} (V): Adj; I_{SD} : 10 μ A; S8, N8; Comparator and Reference, Logic Shutdown, Ref Sources and Sinks 2/4mA
LT1121/LT1121HV	150mA, Micropower, LDO	V_{IN} : 4.2V to 30/36V; $V_{OUT(MIN)}$: 3.75V, Dropout Voltage (V at I_{OUT}): 0.42V; I_Q (Supply): 30 μ A; V_{OUT} (V): Adj, 3.3, 5; I_{SD} : 16 μ A; SOT-223, S8, Z; Reverse-Battery Protection
LT1129	700mA, Micropower, LDO	V_{IN} : 4.2V to 30V; $V_{OUT(MIN)}$: 3.75V, Dropout Voltage (V at I_{OUT}): 0.40V; I_Q (Supply): 50 μ A; V_{OUT} (V): Adj, 3.3, 5; I_{SD} : 16 μ A; DD, SOT-223, S8, TO220-5, TSSOP20
LT1616	25V, 500mA (I_{OUT}), 1.4MHz, High Efficiency Step-Down DC/DC Converter	V_{IN} : 3.6V to 25V, $V_{OUT(MIN)}$: 1.25V, I_Q : 1.9mA, I_{SD} : <1 μ A, ThinSOT
LT1676	60V, 440mA (I_{OUT}), 100kHz, High Efficiency Step-Down DC/DC Converter	V_{IN} : 7.4V to 60V, $V_{OUT(MIN)}$: 1.24V, I_Q : 3.2mA, I_{SD} : 2.5 μ A, S8
LT1761	100mA, Low Noise Micropower, LDO	V_{IN} : 1.8V to 20V; $V_{OUT(MIN)}$: 1.22V, Dropout Voltage (V at I_{OUT}): 0.30V; I_Q (Supply): 20 μ A; V_{OUT} (V): Adj, 1.5, 1.8, 2, 2.5, 2.8, 3, 3.5, 5; I_{SD} : <1 μ A; ThinSOT; Low Noise < 20 μ V _{RMS} P-P, Stable with 1 μ F Ceramic Capacitors